UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 5,929,690

DATED

: July 27, 1999

INVENTOR(S): Williams, Richard K.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title,

Should read:

-- POWER CONVERTER INCLUDING MOSFET AND METHOD OF OPERATING THE SAME --

Column 17,

Line 38, "N-" is deleted an replaced by -- N+ --.

Column 20,

Line 15, -- now abandoned, -- is inserted after "1992,".

Line 16, -- now U.S. Patent No. 5,248,627, issued September 28, 1993, -- is inserted after "1992,".

Column 23,

Line 47, --, now abandoned -- is inserted after "08/367, 027".

Line 48, --, now U.S. Patent No. 5,689,128, issued November 18, 1997 -- is inserted after "08/533,814".

Column 25,

Line 2, --, now abandoned, -- is inserted after "07/855,373".

Line 2, -- now U.S. Patent No. 5,248,627, issued September 28, 1993, -- is inserted after "07/854,162,".

Column 26,

Line 18, "semiconductor device" is deleted and replaced by -- MOSFET ---

Signed and Sealed this

Eighteenth Day of September, 2001

Attest:

Nicholas P. Ebdici

NICHOLAS P. GODICI Acting Director of the United States Patent and Trademark Office

Attesting Officer